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1 **The computer image analysis for the 2-D coronary arteriograms**
Yung-Nien Sun; Shei-Hsi Chang; Chien-Chuan Ko; Chi-Wu Mao;
TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering.1993 IEEE Region 10 Conference on , Issue: 0 , 19-21 Oct. 1993
Pages:978 - 982 vol.2

[Abstract]   [PDF Full-Text (360 KB)]   **IEEE CNF**

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2 **Intelligent modelling, estimation and fusion**
Harris, C.J.;
Update on Developments in Intelligent Control (Ref. No. 1998/513), IEE Colloquium on , 23 Oct. 1998
Pages:2/1

[Abstract]   [PDF Full-Text (56 KB)]   **IEE CNF**

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3 **Static and dynamic approaches to modeling end-to-end routing in circuit-switched networks**
Young Lee; Tien, J.A.;
Networking, IEEE/ACM Transactions on , Volume: 10 , Issue: 5 , Oct. 2002
Pages:693 - 706

[Abstract]   [PDF Full-Text (470 KB)]   **IEEE JNL**

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4 **Inferring motor plan complexity using a modified principal component analysis**
Atsma, W.J.; Hodgson, A.J.;
[Engineering in Medicine and Biology, 1999. 21st Annual Conf. and the 1999 Annual Fall Meeting of the Biomedical Engineering Soc.] BMES/EMBS Conference, 1999. Proceedings of the First Joint , Volume: 1 , 13-16 Oct. 1999
Pages:533 vol.1

[Abstract]   [PDF Full-Text (92 KB)]   **IEEE CNF**

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5 **Turbine system identification: experimental results**
Mehta, A.; Kaufman, H.; Ravi, R.;

Decision and Control, 1994., Proceedings of the 33rd IEEE Conference on , Volume: 4 , 14-16 Dec. 1994
Pages:3593 - 3595 vol.4

[Abstract]   [PDF Full-Text (188 KB)]   **IEEE CNF**

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Terms used **cost vector system identification**   Found **101** of **132,857**

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1 Towards optimal system-level design
Manjote S. Haworth, William P. Birmingham
July 1993 **Proceedings of the 30th international on Design automation conference**

Full text available: 📄 pdf(568.01 KB) Additional Information: full citation, references, index terms

2 Locally Least-Cost Error Recovery in Earley's Algorithm
S. O. Anderson, R. C. Backhouse
July 1981 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,
        Volume 3 Issue 3
Full text available: 📄 pdf(1.51 MB)   Additional Information: full citation, references, citings, index terms

3 Multi-objective *abstract data type refinement* for mapping tables in telecom network applications
Ch. Ykman-Couvreur, J. Lambrecht, A. van der Togt, F. Catthoor
June 2002 **ACM SIGPLAN Notices , Proceedings of the workshop on Memory system performance**, Volume 38 Issue 2 supplement
Full text available: 📄 pdf(1.08 MB)   Additional Information: full citation, abstract, references, index terms

We present a new multi-objective exploration method at the system level to select customized implementations for mapping tables, dynamically allocated, as encountered in telecom network, database, and multimedia applications. Our method fits in the context of embedded system synthesis for such applications, and it enables the optimization of the system-level memory management of these applications. To this end it mainly aims at trading off the average memory footprint, number of memory accesses, ...

Keywords: memory management, memory performance, system-level exploration

4 Automatic performance prediction to support cross development of parallel programs
Matthias Schumann
January 1996 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools**
Full text available: 📄 pdf(1.32 MB)   Additional Information: full citation, references, index terms

5 The minimised geometric Buchberger algorithm: an optimal algebraic algorithm for integer programming
Qiang Li, Yi-ke Guo, Tetsuo Ida, John Darlington
July 1997 **Proceedings of the 1997 international symposium on Symbolic and**

6 A fuzzy model-based optimal control strategy
A. Afshari, C. Georgescu
April 1994 **Proceedings of the 1994 ACM symposium on Applied computing**

Full text available: pdf(625.26 KB)   Additional Information: full citation, references, index terms

Keywords: fuzzy adjoint system, fuzzy logic, optimal control, receding horizon optimization, temperature control application, tracking problem

7 Design of on-line algorithms using hitting times
Prasad Tetali
January 1994 **Proceedings of the fifth annual ACM-SIAM symposium on Discrete algorithms**

Full text available: pdf(1.02 MB)   Additional Information: full citation, references, index terms

8 Query strategies for priced information (extended abstract)
Moses Charikar, Ronald Fagin, Venkatesan Guruswami, Jon Kleinberg, Prabhakar Raghavan, Amit Sahai
May 1999 **Proceedings of the thirty-second annual ACM symposium on Theory of computing**

Full text available: pdf(1.14 MB)   Additional Information: full citation, references, citings, index terms

9 XML query processing I: Dynamic XML documents with distribution and replication
Serge Abiteboul, Angela Bonifati, Grégory Cobéna, Ioana Manolescu, Tova Milo
June 2003 **Proceedings of the 2003 ACM SIGMOD international conference on on Management of data**

Full text available: pdf(209.06 KB)   Additional Information: full citation, abstract, references, index terms

The advent of XML as a universal exchange format, and of Web services as a basis for distributed computing, has fostered the apparition of a new class of documents: *dynamic XML documents*. These are XML documents where some data is given explicitly while other parts are given only intensionally by means of embedded calls to web services that can be called to generate the required information. By the sole presence of Web services, dynamic documents already include inherently some form of di ...

10 Engineering a simple, efficient code-generator generator
Christopher W. Fraser, David R. Hanson, Todd A. Proebsting
September 1992 **ACM Letters on Programming Languages and Systems (LOPLAS)**, Volume 1 Issue 3

Full text available: pdf(853.86 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

Many code-generator generators use tree pattern matching and dynamic programming. This paper describes a simple program that generates matchers that are fast, compact, and easy to understand. It is simpler than common alternatives: 200–700 lines of Icon or 950 lines of C versus 3000 lines of C for Twig and 5000 for burg. Its matchers run up to 25 times faster than Twig's. They are necessarily slower than burg's BURS (bottom-up rewrite system) matchers, but they are more flexible and s ...

Keywords: Icon programming language, code generation, code-generator generator, dynamic programming, tree pattern matching

11 The traveling salesman problem in graphs with 3-edge cutsets

G. Cornuéjols, D. Naddef, W. Pulleyblank

April 1985 **Journal of the ACM (JACM)**, Volume 32 Issue 2

Full text available: pdf(2.18 MB)   Additional Information: full citation, abstract, references, index terms

This paper analyzes decomposition properties of a graph that, when they occur, permit a polynomial solution of the traveling salesman problem and a description of the traveling salesman polytope by a system of linear equalities and inequalities. The central notion is that of a 3-edge cutset, namely, a set of 3 edges that, when removed, disconnects the graph. Conversely, our approach can be used to construct classes of graphs for which there exists a polynomial algorithm for the traveling sa ...

12 Decision tree reduction

J. R. B. Cockett, J. A. Herrera

October 1990 **Journal of the ACM (JACM)**, Volume 37 Issue 4

Full text available: pdf(2.08 MB)   Additional Information: full citation, abstract, references, citings, index terms

The reduction algorithm is a technique for improving a decision tree in the abseence of aproecise cost criterion. The result of applying the algorithm is an irreducible tree that is no less efficient than the original, and may be more efficient. Irreducible trees arise in discrete decision theory as an algebraic form for decision trees. This form has significant computational properties. In fact, every irreducible is optimal with respect to some expected testing cost criterion and is stric ...

13 Aspects of large-scale in-core linear programming

James E. Kalan

January 1971 **Proceedings of the 1971 26th annual conference**

Full text available: pdf(789.83 KB)   Additional Information: full citation, abstract, references, citings, index terms

Unconventional methods for matricial compression indicate that large linear programming constraint matrices may comfortably remain core-resident during optimization. Minor changes in the computational aspects of the simplex algorithm coupled with efficient inverse matrix representation show that the major portion of the inverse in product form of a basis may be embedded in the constraint matrix. A method for generating a sparse inverse matrix is presented.

Keywords: Linear programming, Matrix compression, Matrix inversion, Simplex algorithm, Sparse matrices

14 A parallel embedded-processor architecture for ATM reassembly

Richard F. Hobson, P. S. Wong

February 1999 **IEEE/ACM Transactions on Networking (TON)**, Volume 7 Issue 1

Full text available: pdf(331.21 KB)   Additional Information: full citation, references, citings, index terms

Keywords: ATM, embedded systems, medium access control, segmentation and reassembly

15 Efficient instruction scheduling for delayed-load architectures

Steven M. Kurlander, Todd A. Proebsting, Charles N. Fischer

September 1995 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 17 Issue 5

Full text available: pdf(2.30 MB)   Additional Information: full citation, abstract, references, citings, index terms, review

A fast, optimal code-scheduling algorithm for processors with a delayed load of one instruction cycle is described. The algorithm minimizes both execution time and register use

and runs in time proporti●l to the size of the expression-tree. A●xtension that spills registers when too few registers are available is also presented. The algorithm also performs very well for delayed loads of greater than one instruction cycle. A heuristic that schedules DAGs and is based on our optimal expres ...

16 An automatic grading scheme for simple programming exercises
J. B. Hext, J. W. Winings
May 1969 **Communications of the ACM**, Volume 12 Issue 5

Full text available: pdf(671.35 KB)   Additional Information: full citation, abstract, references, citings, index terms

A discussion is given of alterations that were made to a typical university operating system to record the results of programming exercises in three different languages, includeing assembly language. In this computer-controlled grading scheme provision is made for testing with programmer-supplied data and for final runs with system-supplied data. Exercises run under the scheme may be mixed with other programs, and no special recognition of exercises by the operators is necessary.

Keywords: automatic grading program, programming exercises

17 A multi-similarity algebra
S. Adali, P. Bonatti, M. L. Sapino, V. S. Subrahmanian
June 1998 **ACM SIGMOD Record , Proceedings of the 1998 ACM SIGMOD international conference on Management of data**, Volume 27 Issue 2

Full text available: pdf(1.81 MB)   Additional Information: full citation, abstract, references, citings, index terms

The need to automatically extract and classify the contents of multimedia data archives such as images, video, and text documents has led to significant work on similarity based retrieval of data. To date, most work in this area has focused on the creation of index structures for similarity based retrieval. There is very little work on developing formalisms for querying multimedia databases that support similarity based computations and optimizing such queries, even though it is well known ...

18 Session 6: A BGP-based mechanism for lowest-cost routing
Joan Feigenbaum, Christos Papadimitriou, Rahul Sami, Scott Shenker
July 2002 **Proceedings of the twenty-first annual symposium on Principles of distributed computing**

Full text available: pdf(1.04 MB)   Additional Information: full citation, abstract, references, citings

The routing of traffic between Internet domains or *Autonomous Systems* (ASs), a task known as *interdomain routing*, is currently handled by the Border Gateway Protocol (BGP). In this paper, we address the problem of interdomain routing from a mechanism-design point of view. The application of mechanism-design principles to the study of routing is the subject of earlier work by Nisan and Ronen [14] and Hershberger and Suri [10]. In this paper, we formulate and solve a version of the r ...

19 Control of initialization bias in multivariate simulation response
Lee W. Shruben
April 1981 **Communications of the ACM**, Volume 24 Issue 4

Full text available: pdf(511.90 KB) Additional Information: full citation, references, citings, index terms

Keywords: initialization, multivariate response, simulation

20 Timing-driven placement based on partitioning with dynamic cut-net control
Shih-Lian Ou, Massoud Pedram
June 2000 **Proceedings of the 37th conference on Design automation**

Full text available: Additional Information: full citation, abstract, references, citings, index

This paper presents a partitioning-based, timing-driven placement algorithm. The partitioning step itself is timing-driven and based on solving a quadratic programming problem iteratively. The placement algorithm does not rely on interleaved timing calculations, which tend to be inaccurate. Instead, it achieves the desired result by controlling the number of times that a path in the circuit can be cut. In addition to the cutting constraint, a pre-locking mechanism and timing-aware termin ...

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1 **Asymptotic properties of black-box identification of transfer functions**
Lennart Ljung; Zhen-Dong Yuan;
Automatic Control, IEEE Transactions on , Volume: 30 , Issue: 6 , Jun 1985
Pages:514 - 530

[Abstract]   [PDF Full-Text (1192 KB)]   **IEEE JNL**

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2 **Terrain navigation using Bayesian statistics**
Bergman, N.; Ljung, L.; Gustafsson, F.;
Control Systems Magazine, IEEE , Volume: 19 , Issue: 3 , June 1999
Pages:33 - 40

[Abstract]   [PDF Full-Text (760 KB)]   **IEEE JNL**

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3 **Issues in system identification**
Ljung, L.;
Control Systems Magazine, IEEE , Volume: 11 , Issue: 1 , Jan. 1991
Pages:25 - 29

[Abstract]   [PDF Full-Text (444 KB)]   **IEEE JNL**

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4 **Just in time models for dynamical systems**
Stenman, A.; Gustafsson, F.; Ljung, L.;
Decision and Control, 1996., Proceedings of the 35th IEEE , Volume: 1 , 11-13 Dec. 1996
Pages:1115 - 1120 vol.1

[Abstract]   [PDF Full-Text (400 KB)]   **IEEE CNF**

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5 **A non-asymptotic approach to local modelling**
Roll, J.; Nazin, A.; Ljung, L.;
Decision and Control, 2002, Proceedings of the 41st IEEE Conference on , Volume: 1 , 10-13 Dec. 2002
Pages:638 - 643 vol.1

[Abstract]   [PDF Full-Text (389 KB)]   **IEEE CNF**

6 Identifiability implies robust identifiability

Ljung, L.; Glad, T.; Andersson, T.;
Decision and Control, 1993., Proceedings of the 32nd IEEE Conference on , 15-17 Dec. 1993
Pages:567 - 569 vol.1

[Abstract]   [PDF Full-Text (220 KB)]   **IEEE CNF**

7 A system identification perspective on neural nets

Ljung, L.; Sjoberg, J.;
Neural Networks for Signal Processing [1992] II., Proceedings of the 1992 IEEE-SP Workshop , 31 Aug.-2 Sept. 1992
Pages:423 - 435

[Abstract]   [PDF Full-Text (508 KB)]   **IEEE CNF**

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Frederick Wieland
December 1998 **Proceedings of the 30th conference on Winter simulation**
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Edward J. Williams, Haldun Çelik
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103 Input modeling
Lawrence Leemis
December 1998 **Proceedings of the 30th conference on Winter simulation**
Full text available: 📄 pdf(98.57 KB)   Additional Information: full citation, references, citings, index terms

104 Proposal of a timing model for CMOS logic gates driving a CRC &pgr; load
Akio Hirata, Hidetoshi Onodera, Keikichi Tamaru
November 1998 **Proceedings of the 1998 IEEE/ACM international conference on Computer-aided design**
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105 Building fault tolerant distributed systems using IP multicast
Samuel Tardieu, Laurent Pautet
November 1998 **ACM SIGAda Ada Letters , Proceedings of the 1998 annual ACM SIGAda international conference on Ada,** Volume XVIII Issue 6
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106 Call admission control or adaptive multimedia in wireless/mobile networks
Taekyoung Kwon, Yanghee Choi, Chatschik Bisdikian, Mahmoud Naghshineh
October 1998 **Proceedings of the 1st ACM international workshop on Wireless mobile multimedia**
Full text available: 📄 pdf(617.26 KB)   Additional Information: full citation, references, citings, index terms

107 Lazy computation with exact real numbers

Abbas Edalat, Peter John Potts, Philipp Sünderhauf

September 1998 **ACM SIGPLAN Notices , Proceedings of the third ACM SIGPLAN international conference on Functional programming**, Volume 34 Issue 1

Full text available: pdf(1.22 MB)   Additional Information: full citation, abstract, references, index terms

We provide a semantical framework for exact real arithmetic using linear fractional transformations on the extended real line. We present an extension of PCF with a real type which introduces an eventually breadth-first strategy for lazy evaluation of exact real numbers. In this language, we present the constant *redundant if*, rif, for defining functions by cases which, in contrast to *parallel if* (pif), overcomes the problem of undecidability of comparison of real numbers in finite ...

108 An abstract machine for tabled execution of fixed-order stratified logic programs

Konstantinos Sagonas, Terrance Swift

May 1998 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 20 Issue 3

Full text available: pdf(602.38 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

SLG resolution uses tabling to evaluate nonfloundering normal logic pr ograms according to the well-founded semantics. The SLG-WAM, which forms the engine of the XSB system, can compute in-memory recursive queries an order of magnitude faster than current deductive databases. At the same time, the SLG-WAM tightly intergrates Prolog code with tabled SLG code, and executes Prolog code with minimal overhead compared to the WAM. As a result, the SLG-WAM brings to logic progr ...

Keywords: SLG, WAM, memoing, prolog, stratification theories, tabling

109 A programming environment for the design of complex high speed ASICs

Patrick Schaumont, Serge Vernalde, Luc Rijnders, Marc Engels, Ivo Bolsens

May 1998 **Proceedings of the 35th annual conference on Design automation conference**

Full text available: pdf(223.44 KB)   Additional Information: full citation, abstract, references, citings, index terms

A C++ based programming environment for the design of complex high speed ASICs is presented. The design of a 75 Kgate DECT transceiv er is used as a driv er example. Compact descriptions, combined with efficient sim ulationand syn thesis strategies are essen tial for the design of such a complex system. It is sho wn how a C++ programming approach outperforms traditional HDL-based methods.

110 FRIDGE: a fixed-point design and simulation environment

H. Keding, M. Willems, M. Coors, H. Meyr

February 1998 **Proceedings of the conference on Design, automation and test in Europe**

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Digital systems, especially those for mobile applications are sensitive to power consumption, chip size and costs. Therefore they are realized using fixed-point architectures, either dedicated HW or programmable DSPs. On the other hand, system design starts from a floating-point description. These requirements have been the motivation for FRIDGE (Fixed-point pRogrammIng DesiGn Environment), a design environment for the specification, evaluation and implementation of fixed-point systems. FRIDGE o ...

Keywords: fixed-point, FRIDGE, quantization, design, simulation, interpolation, interpolative approach, fixed-C, assignment-time instantiation

111 Hyperdocuments as automata: verification of trace-based browsing properties by model checking

P. David Stotts, Richard Furuta, Cyrano Ruiz Cabarrus

January 1998 **ACM Transactions on Information Systems (TOIS)**, Volume 16 Issue 1

Full text available: pdf(474.20 KB)   Additional Information: full citation, abstract, references, citings, index terms

We present a view of hyperdocuments in which each document encodes its own browsing semantics in its links. This requires a mental shift in how a hyperdocument is thought of abstractly. Instead of treating the links of a document as defining a static directed graph, they are thought of as defining an abstract program, termed the links-automaton of the document. A branching temporal logic notation, termed HTL*, is introduced for specifying properties a document should exhibi ...

Keywords: Petri nets, browsing semantics, hypermedia, hypertext, model checking, temporal logic

112 Seven habits of highly successful input modelers

Larry Leemis

December 1997 **Proceedings of the 29th conference on Winter simulation**

Full text available: pdf(661.21 KB)   Additional Information: full citation, references, index terms

113 A hierarchical decomposition methodology for multistage clock circuits

Gary Ellis, Lawrence T. Pileggi, Rob A. Rutenbar

November 1997 **Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design**

Full text available: pdf(149.54 KB)   Additional Information: full citation, abstract, references, citings, index terms

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This paper describes a novel methodology to automate the design of the interconnect distribution for multistage clock circuits. We introduce two key ideas. First, a hierarchical decomposition of the layout divides the problem into a set of local Steiner-wired latch clusters (to minimize and balance local capacitance) fed globally by a balanced binary tree (to maximize performance). Second, we recast the global clock distribution problem as a simultaneous optimization of clock topology, clock seg ...

Keywords: clock, routing, performance driven router, manufacturability, process variations

114 Iolus: a framework for scalable secure multicasting

Suvo Mittra

October 1997 **ACM SIGCOMM Computer Communication Review , Proceedings of the ACM SIGCOMM '97 conference on Applications, technologies, architectures, and protocols for computer communication**, Volume 27 Issue 4

Full text available: pdf(1.70 MB)   Additional Information: full citation, abstract, references, citings, index terms

As multicast applications are deployed for mainstream use, the need to secure multicast communications will become critical. Multicast, however, does not fit the point-to-point model of most network security protocols which were designed with unicast communications in mind. As we will show, securing multicast (or group) communications is fundamentally different from securing unicast (or paired) communications. In turn, these differences can result in scalability problems for many typical applica ...

115 Using a fine-grained comparative evaluation technique to understand and design software visualization tools

Paul Mulholland

October 1997 **Papers presented at the seventh workshop on Empirical studies of**

116 A new checkpoint mechanism for real time operating systems   ▭

Santiago Rodríquez, Antonio Pérez, Rafael Méndez

October 1997 **ACM SIGOPS Operating Systems Review**, Volume 31 Issue 4

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> This paper presents an overview of a proposed protocol to provide application---
> transparent fault tolerant services in a Real Time Operating system. Fault tolerance is
> achieved by saving checkpoints of the processes belonging to a real time application. This
> approach proposes the extension of some real time system calls in order to save a
> recovery point when the user invokes them. This protocol allows a real time application
> designer to know the temporal specifications of every system call. Curr ...

117 Approximation algorithms for structured communication problems   ▭

Dominique Barth, Pierre Fraigniaud

June 1997 **Proceedings of the ninth annual ACM symposium on Parallel algorithms
and architectures**

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119 Fast and precise computations of discrete Fourier transforms using cyclotomic   ▭
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Joe Buhler, M. Amin Shokrollahi, Volker Stemann

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120 Effect of data distribution in data assimilation using Burgers' equation   ▭

Wanglung Chung, John M. Lewis, S. Lakshmivarahan, S. K. Dhall

April 1997 **Proceedings of the 1997 ACM symposium on Applied computing**

Full text available: 📄 pdf(388.49 KB)   Additional Information: full citation, references, index terms

Keywords: adjoint method, data assimilationBurgers' equation, grid-point

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1 Queue response to input correlation functions: continuous spectral analysis
San-qi Li, Chia-Lin Hwang
December 1993 **IEEE/ACM Transactions on Networking (TON),** Volume 1 Issue 6
Full text available: 📄 pdf(1.42 MB)   Additional Information: full citation, references, citings, index terms

2 Synthesis of saturation arithmetic architectures
G. A. Constantinides, P. Y. K. Cheung, W. Luk
July 2003 **ACM Transactions on Design Automation of Electronic Systems (TODAES),**
Volume 8 Issue 3
Full text available: 📄 pdf(391.91 KB)   Additional Information: full citation, abstract, references, index terms

This paper describes a synthesis technique for automating the design of linear Digital Signal Processing (DSP) systems such as digital filters. The proposed methodology makes optimized use of saturation arithmetic to produce a small design implemented directly in hardware. An analytical technique is proposed to estimate the saturation error resulting from a particular implementation, and an optimization procedure is introduced to aim for the smallest implementation satisfying user-specified boun ...

Keywords: Signal processing, saturation arithmetic, synthesis

3 A histogram-based model for video traffic behavior in an ATM multiplexer
Paul Skelly, Mischa Schwartz, Sudhir Dixit
August 1993 **IEEE/ACM Transactions on Networking (TON),** Volume 1 Issue 4
Full text available: 📄 pdf(1.36 MB)   Additional Information: full citation, references, citings, index terms

4 Analysis methodology II: A comparison of five steady-state truncation heuristics for simulation
K. Preston White, Michael J. Cobb, Stephen C. Spratt
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We compare the performance of five well-known truncation heuristics for mitigating the effects of initialization bias in the output analysis of steady-state simulations. Two of these rules are variants of the MSER heuristic studied by White (1997); the remaining rules are adaptations of bias-detection tests based on the seminal work of Schruben (1982). Each heuristic was tested in each of a 168 different experiments. Each experiment comprised multiple tests on different realizations of the sampl ...

5 FACT: a framework for the application of throughput and power optimizing transformations to control-flow intensive behavioral descriptions

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In this paper, we present an algorithm for the application of a general class of transformations to control-flow intensive behavioral descriptions. Our algorithm is based on the observation that incorporation of scheduling information can help guide the selection and application of candidate transformations, and significantly enhance the quality of the synthesized solution. The efficacy of the selected throughput and power optimizing transformations is enhanced by the ability of our algorit ...

Results 1 - 5 of 5